TLB Notes

* the TLB is shared between instructions and data, and dual ported to allow an instruction translation and data address translation at the same time.
* the last 256MiB (the upper nybble of the address is ‘F’) of the memory space is designated as unmapped. This is to allow a common area for the operating system.
* the address range $FFDxxxxx is designated as an I/O space, unless in kernel mode a TLB miss will occur if trying to access those pages of memory